



Microprocessor Architecture, Programming, and Applications with the 8085

By Ramesh Gaonkar

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About the book microprocessor architecture, programming and applications with the 8085 is a detailed guide that provides information on microprocessors, covering its hardware and software areas based on the 8085 microprocessor family. The book was first published in 1984 by author ramesh gaonkar. The updated edition-5th edition-includes the most recent technological changes. microprocessor architecture, programming and applications with the 8085 focuses on 8085 microprocessor family to teach the basic concept of programmable devices. Providing explanations from fundamentals to applications, the book prepares the readers to apply various concepts in their jobs related to microprocessors. the book is divided into three parts. The first part is called microprocessor based systems: hardware and interfacing. This part covers the topics microprocessors, microcomputer and assembly language; introduction to 8085 assembly language programming; microprocessor architecture and microcomputer systems; 8085 microprocessor architecture and memory interfacing and interfacing i/o devices. Part two is called programming the 8085. It covers introduction to 8085 instructions, programming techniques with additional instructions, counter and time delays, stack and subroutines, code conversion bcd arithmetic and 16-bit data operations and software development system and assemblers. The third part is called interfacing peripherals (i/o) and applications. It covers interrupts, interfacing data converters, programmable interface devices, general purpose programmable peripheral devices, serial i/o and data communication, microprocessor applications and extending 8-bit microprocessor concepts to higher level processor and microcontrollers. This 5th edition is published in paperback, on 1st december, 2000. key features an additional interfacing application in the context of mcts is included. a programming model of the 8085 microprocessor and its instruction sets are given.

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