



Chip Design for Submicron VLSI: CMOS Layout and Simulation

By John P. Uyemura



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The text is organized around first introducing the global view of digital integrated circuit design, VLSI and design automation, and then sequentially developing the topics from the materials and devices level, up through the circuits and then system level. This mirrors the structural hierarchy of the chip design field itself. While building a solid foundation and reference for the chip design, it integrates the discussion with hands-on examples of the design automation software, included in the book, to illustrate not only the layout and simulation concepts, but also how an industry designer would put them into practice. Both theory and application are effectively integrated into a cohesive treatment of the subject and art of chip design.

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Editorial Review

Review

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About the Author

John P. Uyemura is Professor of Electrical and Computer Engineering, late of Georgia Institute of Technology.

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